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X-1050 US
10/090,250 *tw*

PATENT
Conf. No.: 4098

IN THE UNITED STATES PATENT OFFICE

Applicants: Suresh M. Menon et al.

Assignee: Xilinx, Inc.

Title: "High Speed Configurable Transceiver Architecture"

Serial No.: 10/090,250

File Date: 03/01/2002

Examiner: Ted M. Wang

Art Unit: 2611

Docket No.: X-1050 US

Conf. No.: 4098

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AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

In response to the Final Office Action mailed from the Patent Office on August 17, 2006, please reconsider allowance of the claims in view of the Remarks below.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 3.